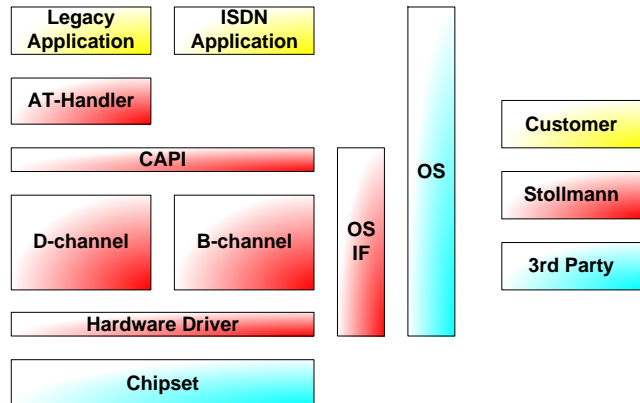


# ISDNCode+

## Portable ISDN stack

- Platform-independent architecture
- For embedded and PC-based systems
- Powerful CAPI interface



## Function

ISDNCode+ is a portable ISDN protocol stack developed and maintained by Stollmann E+V GmbH. It has been ported to many embedded and PC-based systems and supports various ISDN chipsets, CPUs and operating systems.

The stack consists of the D- and B-channel protocol supporting all ISDN functions for Basic Rate (BRI, S0) and Primary Rate (PRI, S2m) lines. All modes (NT, TE) are supported. Hardware driver for all important ISDN chips on the market are available. Please refer to the feature list for more details.

The application interface of the stack is an enhanced CAPI 2.0. Additional support for legacy interfaces like AT-commands are available.

The operating system interface (OSIF) makes integration into different platforms simple without altering the stack itself.

## ISDN protocol stack details

### *D channel protocols*

	DSS1 TE and NT mode
	National 1 and 2, 5ESS, JATE (INS64)
	X.31 D-Channel

### *B channel protocols*

	Transparent, V.110, Bonding
	X.75, LAP-B, LAP-D, V.120
	X.25, X.31, ISO8208, T.70, T.90 NL

### *Supplementary Services*

	MSN, DDI, SUB, CLIP, CLIR, COLP, COLR
	CW, HOLD, ECT, CD, 3PTY, CONF
	CCBS, CCNR
	CFU, CFB, CFNR
	UUS, TP, MCID, MWI, AOC-D, AOC-E

### *API*

	CAPI 2.0 with extensions
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### *Additional software modules*

	AT command handler
	Trace and logging module
	SIP gateway
	CAPI Demo applications

### *Hardware driver*

	Infineon IPAC-X, QUADFALC, MUNICHxx, etc
	Cologne HFCxx, XHFC-xSU, etc

### *OS integration*

	OSIF standalone
	Windows 98, NT, 2000, XP, Vista
	Linux 2.4.xx and 2.6.xx, µClinux, QNX
	pSOS, MTK, RT-Kernel, ThreadX, etc.

### *CPU systems*

	x86 (PC and embedded)
	C16x, ARM7, ARM9, 68xxx, QUICC
	TMX320xxx, AR7, XSCALE

**“ISDN – driven by Stollmann!”**

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